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09/746,815

PATENT
Conf. No.: 8894



IN THE UNITED STATES PATENT OFFICE

Applicants: Jean-Didier Allegrucci et al.
Assignee: Xilinx, Inc.
Title: "MULTIPLE MEMORY ALIASING FOR A CONFIGURABLE
SYSTEM-ON-CHIP"
Serial No.: 09/746,815 File Date: 12/22/2000
Examiner: Brian R. Peugh Art Unit: 2187
Docket No.: X-1761 US Conf. No.: 8894

Mail Stop AMENDMENT
COMMISSIONER FOR PATENTS
P.O. Box 1450
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AMENDMENT IN RESPONSE TO THE SECOND OFFICE ACTION

Dear Sir:

In response to the Office Action mailed from the Patent Office on July 21, 2004,
please reconsider allowance of the claims in view of the Remarks below.

Amendments to the Claims begin on page 2 of this paper.

Remarks being on page 8 of this paper.

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